

<b>Notice of References Cited</b>	Application/Control No. 10/061,066		Applicant(s)/Patent Under Reexamination STOICA, ADRIAN	
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#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,636,125 A	06-1997	Rostoker et al.	700/121
*	B	US-5,668,732 A	09-1997	Khouja et al.	702/60
*	C	US-5,790,436 A	08-1998	Chen et al.	716/5
*	D	US-5,867,397 A	02-1999	Koza et al.	703/14
*	E	US-2002/0072872 A1	06-2002	Chatterjee et al.	702/117
*	F	US-2003/0006413 A1	01-2003	Chawla et al.	257/48
*	G	US-2004/0031001 A1	02-2004	Zhang et al.	716/4
*	H	US-6,728,666 B1	04-2004	Stoica et al.	703/13
*	I	US-6,851,097 B2	02-2005	Zhang et al.	716/5
*	J	US-2005/0114111 A1	05-2005	Zhang et al.	703/014
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)			
*	U	Peter Vancorenland, C. De Ranter, M. Steyaert, G. Gielen □□Optimal RF Deisgn Using Smart Evolutionary Algorithm □□2000 ACM 1-58113-188-7 □□DAC 2000, Los Angeles, CA			
*	V	Tetsuya Higuchi, Nobuki Kajihara □□Evolvable Hardware Chips for Indistrial Applications □□April 1999/Vol. 42, No. 4 Communication of the ACM			
*	W	Melvin A. Breuer, Alice C. Parker □□Digital System Simulation: Currentl Status and Future Trends of Carwin's Theory of Simulation □□0146-7123/81/0000-0269, 1981 IEEE, 18th Design Automation Conference			
*	X	Michael Krasnicki, Rodney Phelps, Rob A. Rutenbar, L. Richard Carley □□Maelstrom: Efficient Simulation-Based Synthesis for Custom Analog Cells □□DAC 99, New Orleans, LA □□1999 ACM 1-58113-109-7/99/06			

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.

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	A	US-			
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	G	US-			
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	I	US-			
	J	US-			
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	M	US-			

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Neil H. E. Weste, Kamran Eshraghian□□Principles of CMOS VLSI Design: A Systems Perspective, Second Edition, 1993□□Addison-Wesley Publishing Company□□
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.